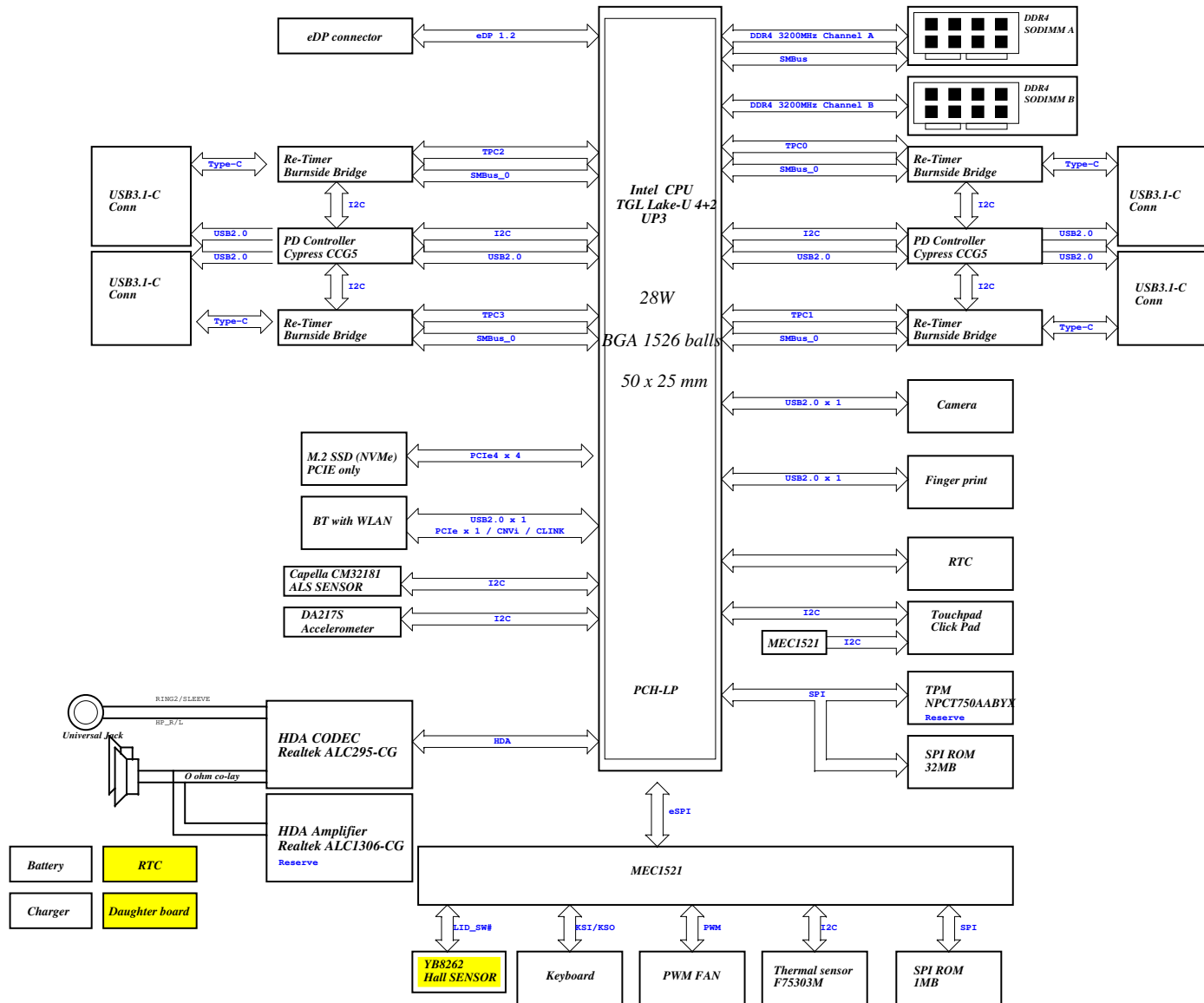
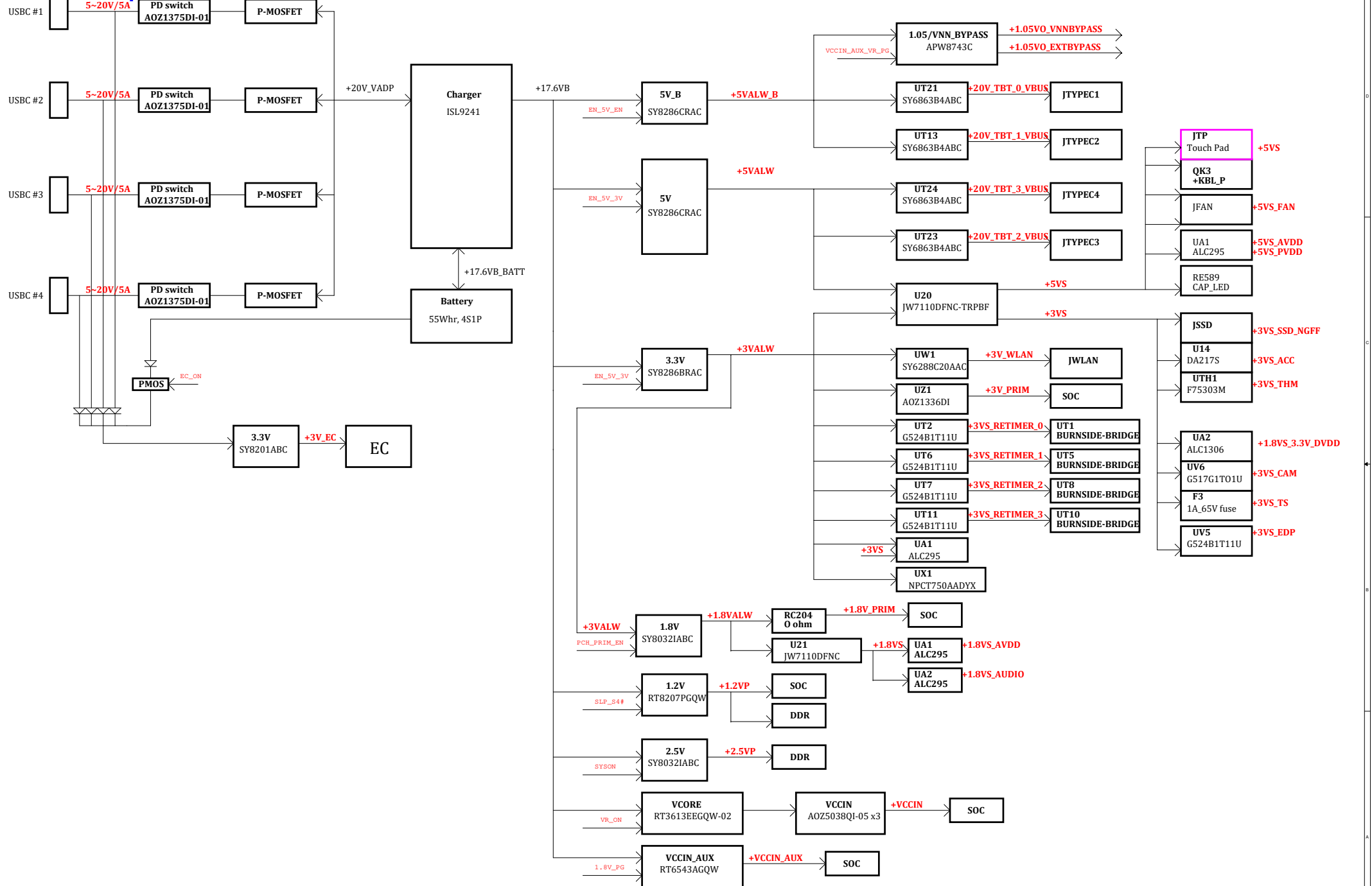


# Block Diagram



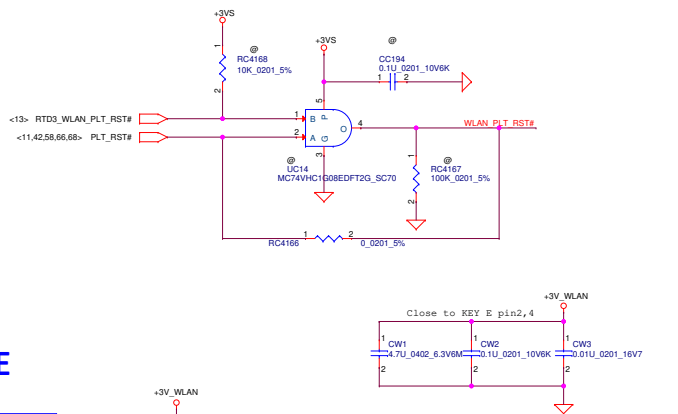
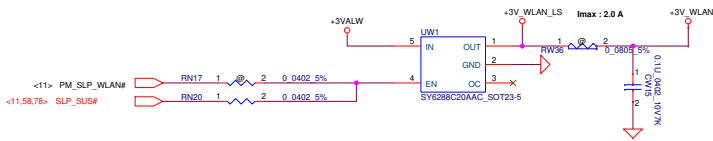
## Power Map



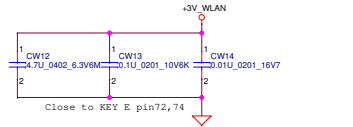
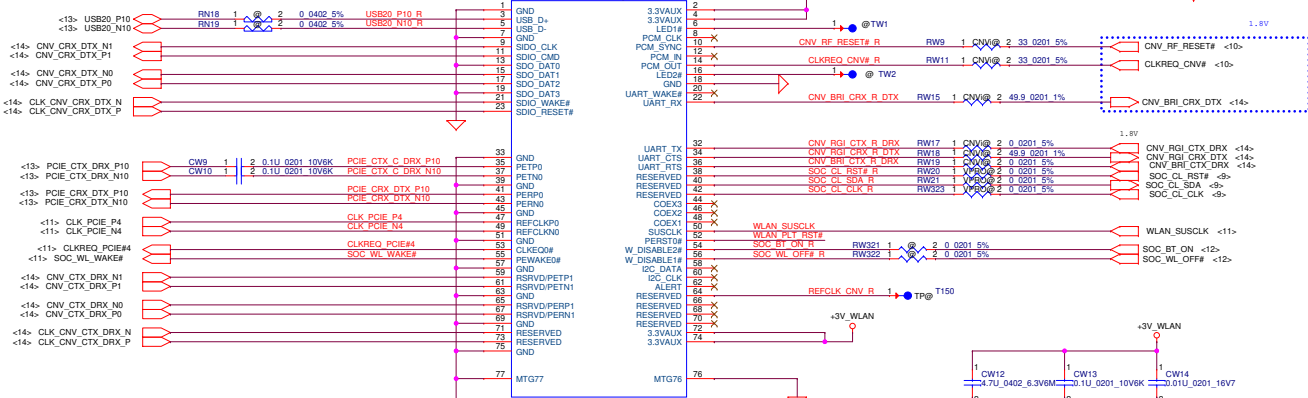
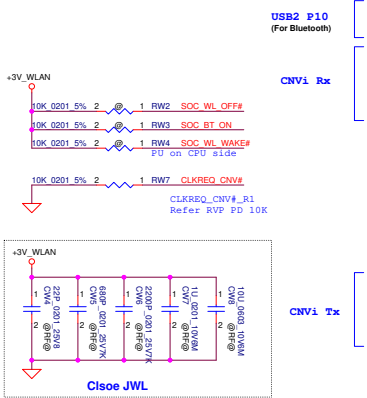
<https://github.com/FrameworkComputer/Mainboard>  
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visit <http://creativecommons.org/licenses/by/4.0/>



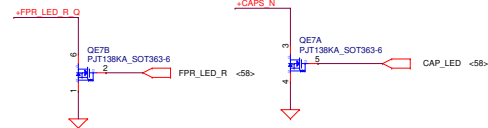
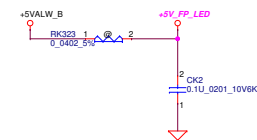
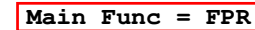
NGFF Wireless LAN / BT (Key E) [PCIE+USB/CNVi]



KEY E

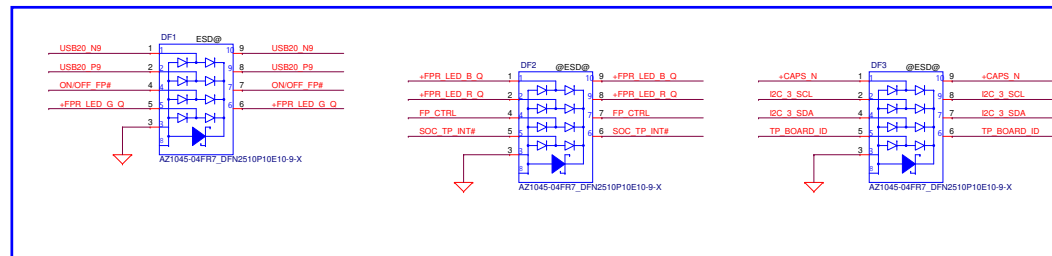


```
<58> KS[0..7]    >>=====
<58> KSO0[0..9]  <<=====
<58> KSO[10..15] <<=====
```



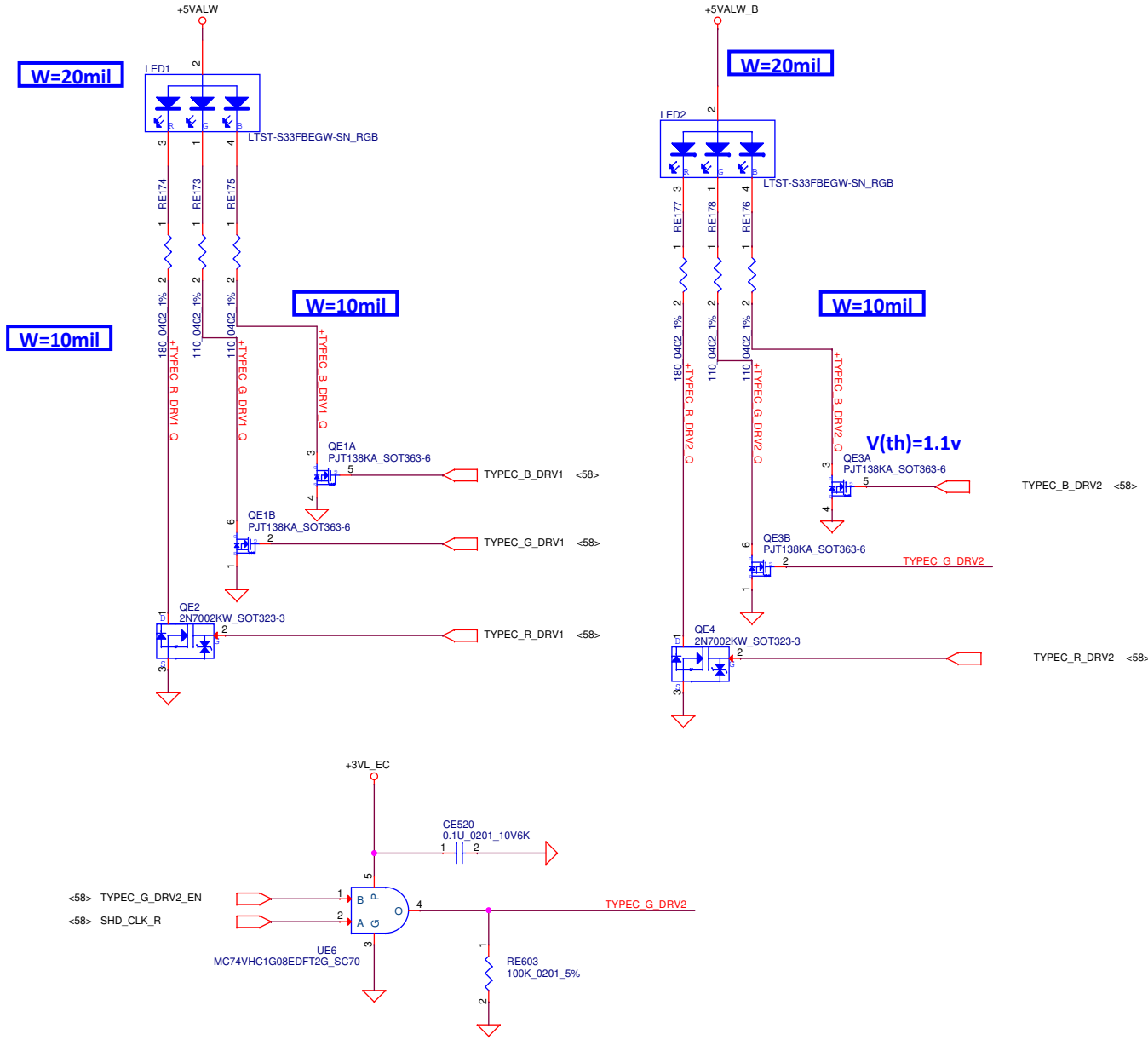
### Keyboard backlight

The schematic diagram illustrates the keyboard backlight control circuit. It features a +5VS supply connected to a 10K resistor (RK325, 0402\_5%) and a MOSFET (GK4, 2N7002K, SOT323-3). The MOSFET's gate is connected to the +5VS supply through a 10K resistor (RK325, 0402\_5%) and to the EC\_KBL\_PWR\_EN\_R signal. The MOSFET's drain is connected to the +KBL\_P supply through a 100K resistor (RK327, 100K\_0402\_5%). The MOSFET's source is connected to ground. The output of the MOSFET is connected to the keyboard backlight.

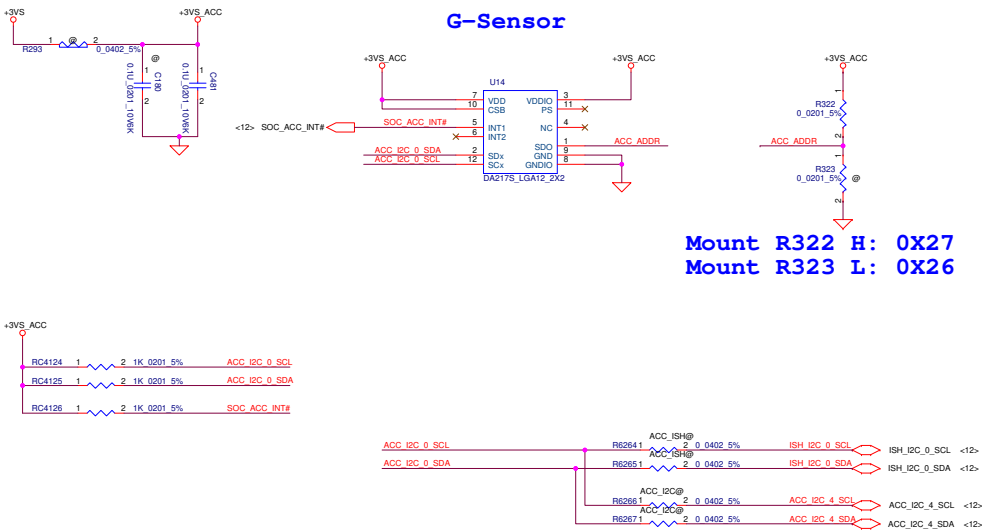


# Left TBT LED

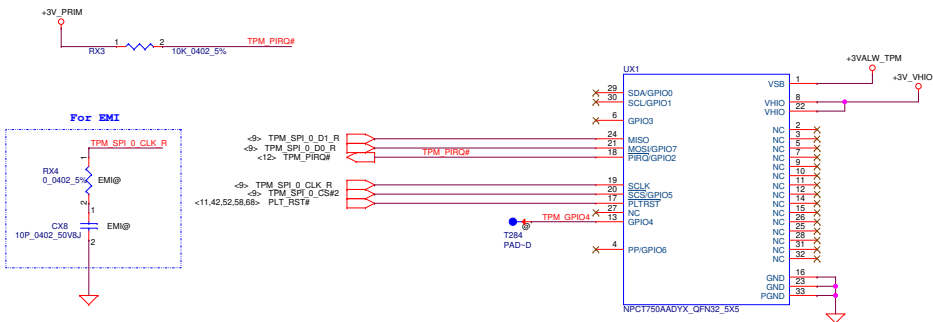
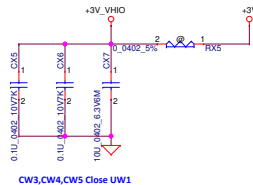
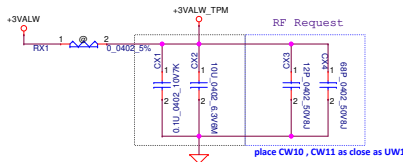
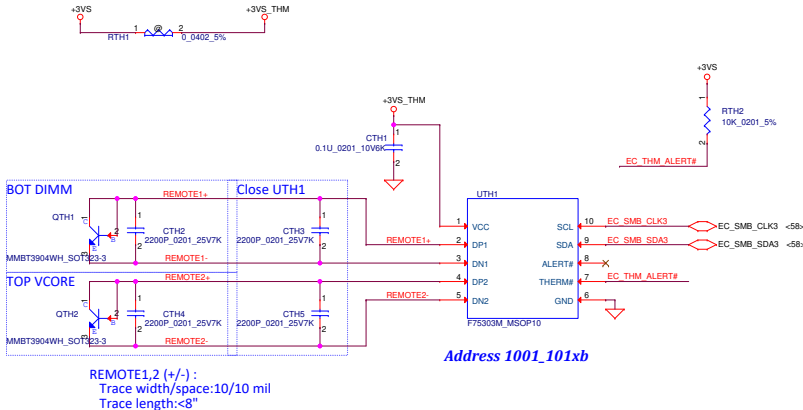
# Right TBT LED



**Main Func = SENSOR**



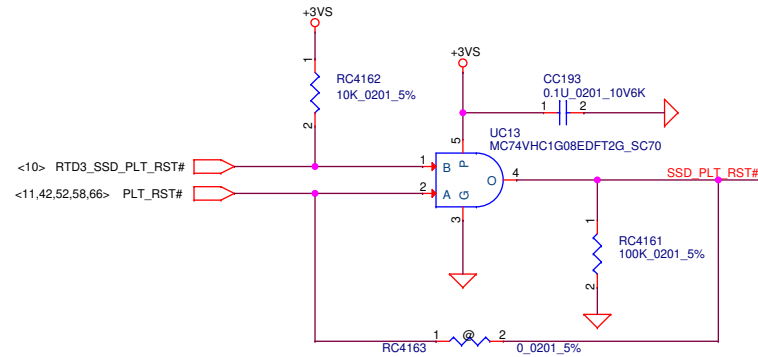
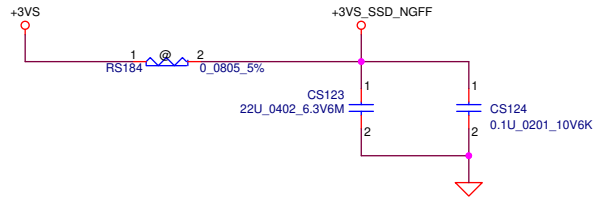
## THERMAL SENSOR



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# NGFF SSD (KEY M)

## PCIE Gen4



## KEY M

