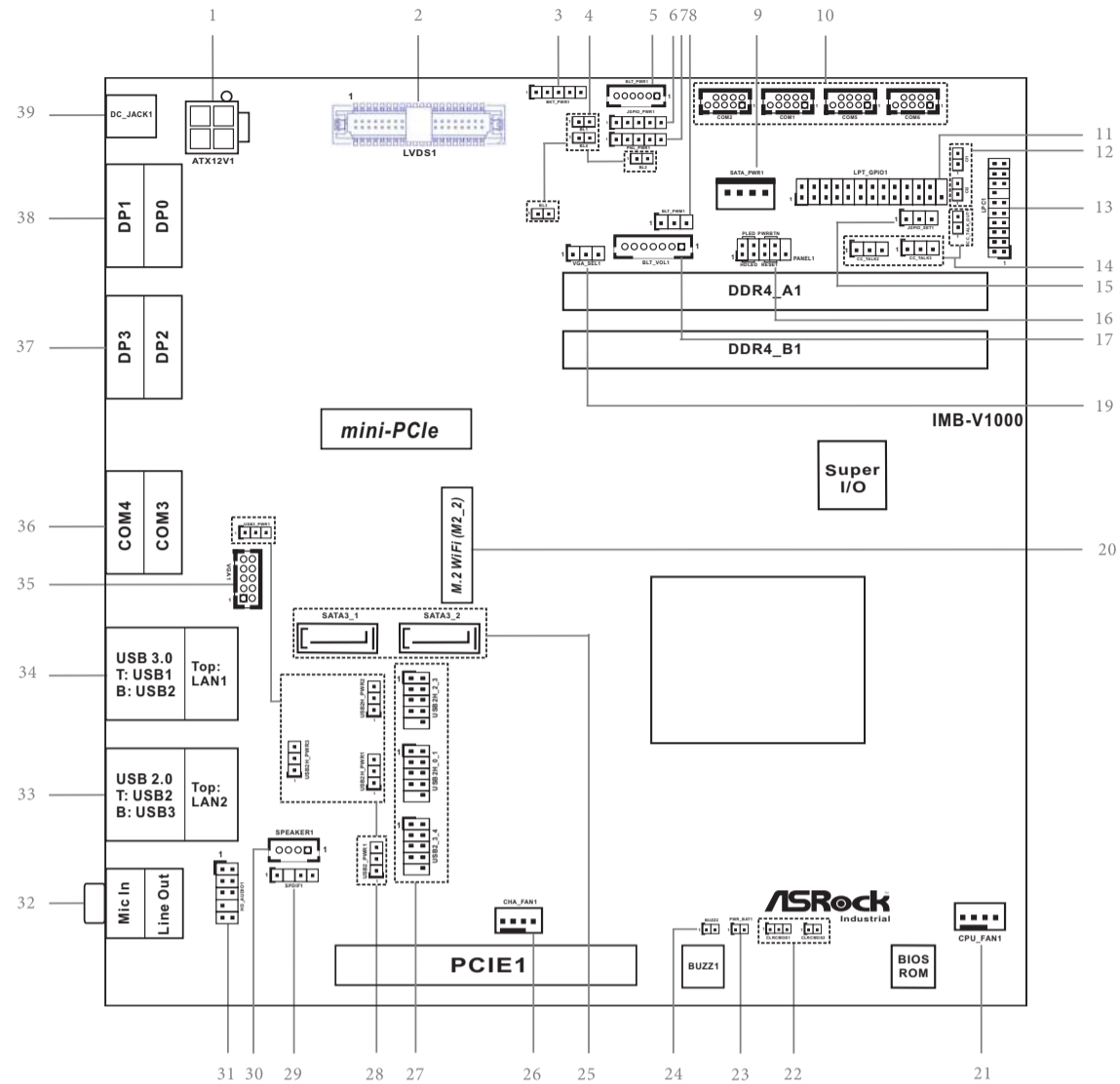


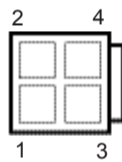
ASRock Jumpers and Headers Setting Guide

IMB-V1000

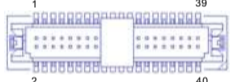
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1 : ATX Power Connector
1-2 : GND
3-4 : DC Input



2 : LVDS Panel Connector



PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
1	R_LVDD	1	R_LVDD	1	R_LVDD
2	NA	2	NA	2	NA
3	+3V	3	NA	3	+3V
4	+3V	4	NA	4	+3V
5	NA	5	LVDS_A_DATA0#	5	NA
6	NA	6	LVDS_A_DATA1#	6	LVDS_A_DATA0#
7	NA	7	LVDS_A_DATA2#	7	LVDS_A_DATA1#
8	NA	8	LVDS_A_DATA3#	8	LVDS_A_DATA2#
9	NA	9	LVDS_A_CLK	9	LVDS_A_DATA3#
10	NA	10	LVDS_B_DATA0#	10	LVDS_A_CLK
11	NA	11	LVDS_B_DATA1#	11	LVDS_B_DATA0#
12	NA	12	LVDS_B_DATA2#	12	LVDS_B_DATA1#
13	NA	13	LVDS_B_DATA3#	13	LVDS_B_DATA2#
14	NA	14	LVDS_B_CLK	14	LVDS_B_DATA3#
15	NA	15	CON_LBKLTT_CTL	15	LVDS_B_CLK
16	NA	16	CON_LBKLTT_VCC	16	CON_LBKLTT_CTL
17	NA	17	CON_LBKLTT_VCC	17	CON_LBKLTT_VCC
18	NA	18	CON_LBKLTT_VCC	18	CON_LBKLTT_VCC
19	NA	19	CON_LBKLTT_VCC	19	CON_LBKLTT_VCC
20	NA	20	CON_LBKLTT_VCC	20	CON_LBKLTT_VCC
21	NA	21	CON_LBKLTT_VCC	21	CON_LBKLTT_VCC
22	NA	22	CON_LBKLTT_VCC	22	CON_LBKLTT_VCC
23	NA	23	CON_LBKLTT_VCC	23	CON_LBKLTT_VCC
24	NA	24	CON_LBKLTT_VCC	24	CON_LBKLTT_VCC
25	NA	25	CON_LBKLTT_VCC	25	CON_LBKLTT_VCC
26	NA	26	CON_LBKLTT_VCC	26	CON_LBKLTT_VCC
27	NA	27	CON_LBKLTT_VCC	27	CON_LBKLTT_VCC
28	NA	28	CON_LBKLTT_VCC	28	CON_LBKLTT_VCC
29	NA	29	CON_LBKLTT_VCC	29	CON_LBKLTT_VCC
30	NA	30	CON_LBKLTT_VCC	30	CON_LBKLTT_VCC
31	NA	31	CON_LBKLTT_VCC	31	CON_LBKLTT_VCC
32	NA	32	CON_LBKLTT_VCC	32	CON_LBKLTT_VCC
33	NA	33	CON_LBKLTT_VCC	33	CON_LBKLTT_VCC
34	NA	34	CON_LBKLTT_VCC	34	CON_LBKLTT_VCC
35	NA	35	CON_LBKLTT_VCC	35	CON_LBKLTT_VCC
36	NA	36	CON_LBKLTT_VCC	36	CON_LBKLTT_VCC
37	NA	37	CON_LBKLTT_VCC	37	CON_LBKLTT_VCC
38	NA	38	CON_LBKLTT_VCC	38	CON_LBKLTT_VCC
39	NA	39	CON_LBKLTT_VCC	39	CON_LBKLTT_VCC
40	NA	40	CON_LBKLTT_VCC	40	CON_LBKLTT_VCC

* eDP Connector
(on the Backside of PCB)



PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
1	NA	1	NA	1	NA
2	NA	2	NA	2	NA
3	+12V	3	+12V	3	+12V
4	+12V	4	+12V	4	+12V
5	+12V	5	+12V	5	+12V
6	+12V	6	+12V	6	+12V
7	NA	7	SMB_CLK	7	NA
8	NA	8	SMB_DATA	8	NA
9	NA	9	SMB_LBKLTT_CTL	9	NA
10	NA	10	eDP_LBKLTT_EN	10	NA
11	NA	11	GND	11	NA
12	NA	12	GND	12	NA
13	NA	13	GND	13	NA
14	NA	14	GND	14	NA
15	NA	15	GND	15	NA
16	NA	16	GND	16	NA
17	NA	17	GND	17	NA
18	NA	18	GND	18	NA
19	NA	19	GND	19	NA
20	NA	20	GND	20	NA
21	NA	21	GND	21	NA
22	NA	22	GND	22	NA
23	NA	23	GND	23	NA
24	NA	24	GND	24	NA
25	NA	25	GND	25	NA
26	NA	26	GND	26	NA
27	NA	27	GND	27	NA
28	NA	28	GND	28	NA
29	NA	29	GND	29	NA
30	NA	30	GND	30	NA
31	NA	31	GND	31	NA
32	NA	32	GND	32	NA
33	NA	33	GND	33	NA
34	NA	34	GND	34	NA
35	NA	35	GND	35	NA
36	NA	36	GND	36	NA
37	NA	37	GND	37	NA
38	NA	38	GND	38	NA
39	NA	39	GND	39	NA
40	NA	40	GND	40	NA

3 : Backlight Power Select
(LCD_BLT_VCC) (BKT_PWR1)
1-2 : LCD_BLT_VCC: +5V
2-3 : LCD_BLT_VCC: +12V
4-5 : LCD_BLT_VCC: DC_IN



4 : BL1 :
Open : Protect LCD_BLT_VCC
Short : No Protect LCD_BLT_VCC
BL2 :
Open : Protect R_LVDD
Short : No Protect R_LVDD



5 : Inverter Power Control Wafer
(BLT_PWR1)



PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
1	GND	1	GND	1	GND
2	GND	2	GND	2	GND
3	CON_LBKLTT_CTL	3	CON_LBKLTT_CTL	3	CON_LBKLTT_CTL
4	CON_LBKLTT_EN	4	CON_LBKLTT_EN	4	CON_LBKLTT_EN
5	LCD_BLT_VCC	5	LCD_BLT_VCC	5	LCD_BLT_VCC
6	LCD_BLT_VCC	6	LCD_BLT_VCC	6	LCD_BLT_VCC

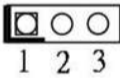
6 : Digital Input / Output Power Select
(JGPIO_PWR) (JGPIO_PWR1)
1-2 : +12V
2-3 : +5V
3-4 : +5V
4-5 : GND



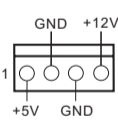
7 : Panel Power Select (R_LVDD)
(PNL_PWR1)
1-2 : R_LVDD: +3V
2-3 : R_LVDD: +5V
4-5 : R_LVDD: +12V



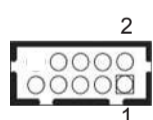
8 : Backlight Control Level
(CON_LBKLTT_CTL) (BLT_PWM1)
1-2 : From eDP PWM to CON_LBKLTT_CTL (+3V)
2-3 : From LVDS PWM to CON_LBKLTT_CTL (+5V)



9 : SATA Power Output Connector



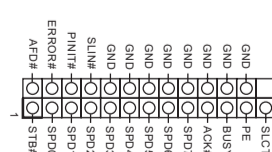
10 : COM Port Headers
(COM1, 2, 5, 6) (RS232)



PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
1	RRXD#	1	RRXD#	1	RRXD#
2	DDTR#	2	DDTR#	2	DDTR#
3	TTXD	3	TTXD	3	TTXD
4	DDSR#	4	DDSR#	4	DDSR#
5	GND	5	GND	5	GND
6	DDSR#	6	DDSR#	6	DDSR#
7	RRTS#	7	RRTS#	7	RRTS#
8	CCTS#	8	CCTS#	8	CCTS#
9	NA	9	NA	9	NA

11 : Printer Port / GPIO Header (LPT_GPIO1)

Printer Port:

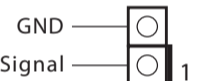


GPIO:

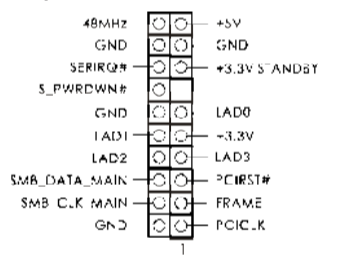
PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
1	SIO_GPA4#	1	SIO_GPA4#	1	SIO_GPA4#
2	SIO_GPA5	2	SIO_GPA5	2	SIO_GPA5
3	SIO_GPA4#	3	SIO_GPA4#	3	SIO_GPA4#
4	SIO_GPA5	4	SIO_GPA5	4	SIO_GPA5
5	SIO_GPA4#	5	SIO_GPA4#	5	SIO_GPA4#
6	SIO_GPA5	6	SIO_GPA5	6	SIO_GPA5
7	SIO_GPA4#	7	SIO_GPA4#	7	SIO_GPA4#
8	SIO_GPA5	8	SIO_GPA5	8	SIO_GPA5
9	SIO_GPA4#	9	SIO_GPA4#	9	SIO_GPA4#
10	SIO_GPA5	10	SIO_GPA5	10	SIO_GPA5
11	SIO_GPA4#	11	SIO_GPA4#	11	SIO_GPA4#
12	SIO_GPA5	12	SIO_GPA5	12	SIO_GPA5
13	SIO_GPA4#	13	SIO_GPA4#	13	SIO_GPA4#
14	SIO_GPA5	14	SIO_GPA5	14	SIO_GPA5
15	SIO_GPA4#	15	SIO_GPA4#	15	SIO_GPA4#
16	SIO_GPA5	16	SIO_GPA5	16	SIO_GPA5
17	SIO_GPA4#	17	SIO_GPA4#	17	SIO_GPA4#
18	SIO_GPA5	18	SIO_GPA5	18	SIO_GPA5
19	SIO_GPA4#	19	SIO_GPA4#	19	SIO_GPA4#
20	SIO_GPA5	20	SIO_GPA5	20	SIO_GPA5
21	SIO_GPA4#	21	SIO_GPA4#	21	SIO_GPA4#
22	SIO_GPA5	22	SIO_GPA5	22	SIO_GPA5
23	SIO_GPA4#	23	SIO_GPA4#	23	SIO_GPA4#
24	SIO_GPA5	24	SIO_GPA5	24	SIO_GPA5
25	NA	25	NA	25	NA
26	NA	26	NA	26	NA
27	NA	27	NA	27	NA
28	NA	28	NA	28	NA
29	NA	29	NA	29	NA
30	NA	30	NA	30	NA
31	NA	31	NA	31	NA
32	NA	32	NA	32	NA
33	NA	33	NA	33	NA
34	NA	34	NA	34	NA
35	NA	35	NA	35	NA
36	NA	36	NA	36	NA
37	NA	37	NA	37	NA
38	NA	38	NA	38	NA
39	NA	39	NA	39	NA
40	NA	40	NA	40	NA

* If you want to use the printer port function, please short pin4 and pin5 on Digital Input / Output Power Select (JGPIO_PWR1).

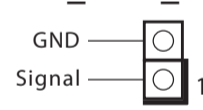
12 : Chassis Intrusion Headers
CI1 :
Close : Active Case Open
Open : Normal
CI2 :
Close : Normal
Open : Active Case Open



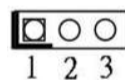
13 : LPC Header



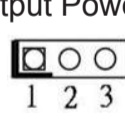
14 : CC_TALK_OUT1



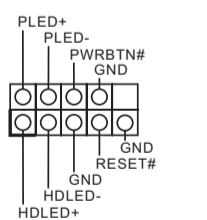
CC_TALK2, CC_TALK3
1-2 : CCTALK
2-3 : COMF



15 : Digital Input / Output Power Select
(JGPIO_SET1)
1-2 : Pull-High
2-3 : Pull-Low



16 : System Panel Header

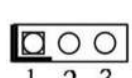


17 : Backlight & Amp Volume Control (BLT_VOL1)



PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
1	GPIO_VOL_UP	1	GPIO_VOL_UP	1	GPIO_VOL_UP
2	GPIO_VOL_DW	2	GPIO_VOL_DW	2	GPIO_VOL_DW
3	PWRDN	3	PWRDN	3	PWRDN
4	GPIO_BLT_UP	4	GPIO_BLT_UP	4	GPIO_BLT_UP
5	GPIO_BLT_DW	5	GPIO_BLT_DW	5	GPIO_BLT_DW
6	GND	6	GND	6	GND
7	GND	7	GND	7	GND

19 : VGA_SEL1
1-2 : DP3
2-3 : VGA1



21 : CPU FAN Connector (+12V)



